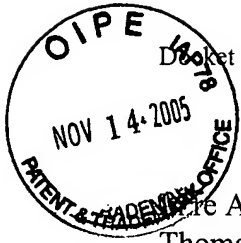


IF 2112



Docket Q02-1031-US1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of:

Thomas Bolt, et al

Serial No.: 10/075,121

Filed: Feb. 13, 2002

For: Use of the Universal Serial Bus as an
Internal Architecture within IDE Disk
Array

) Group Art Unit: 2112

) Examiner: Patel, Nimesh

) **Reply to Office Action**

) **Dated August 10, 2005**

Reply

Commissioner for Patents
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Dear Sir/Madam:

Applicant hereby responds to the Office Action of August 10, 2005 in the above-referenced patent application. Reconsideration and allowance of the claims is respectfully requested in view of the following amendments and remarks. The undersigned has been authorized to respond to the Office Action.

Amendments to the Claims are reflected in the listing of the claims which begins on page 2 of this paper. **Remarks** begin on page 5 of this paper.